
UTM

UNIVERSITI TEKNOLOGI MALAYSIA

# DIGITAL LOGIC

## SEMESTER I 2020/2021

## SECR1013-05

## LAB 1


| NAME OF STUDENTS | MATRIC NO. |
|---|---|
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## E. Laboratory Work

### Part 1

1. Construct Circuit 1 on the breadboard. Connect all inputs (A, B) to a switches and output F to LEDs.


Circuit 1

Truth Table 5

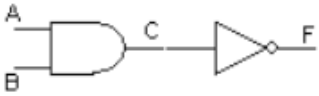
| Input | | Output |
|---|---|---|
| A | B | F |
| 1 | 1 | 1 |
| 1 | 0 | 0 |
| 0 | 1 | 0 |
| 0 | 0 | 0 |

2. Test Circuit 1 and fill in Truth Table 5 for the circuit response to all possible input combinations. The Truth Table 5 should match the Truth Table 1 prepared in the Preliminary Work.

*Fully Completed* ☐ *Partially Completed* ☐ *Checked by:* _____

### Part 2

3. Construct Circuit 2 on the breadboard. Connect all inputs (A, B) to a switches and output C and F to LEDs.




Circuit 2

Truth Table 6

| A | B | C | F |
|---|---|---|---|
| 1 | 1 | 1 | 0 |
| 1 | 0 | 0 | 1 |
| 0 | 1 | 0 | 1 |
| 0 | 0 | 0 | 1 |

4. Test Circuit 2; fill in Truth Table 6, for the circuit response to all possible input combinations.

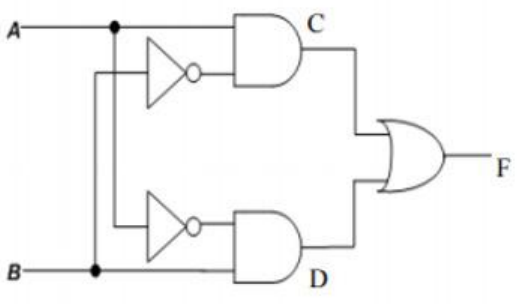
5. Compare Truth Table 6 to Truth Table 2. What conclusion can you make?

I can conclude that the NOT Gate acts as an inverter.

*Fully Completed* ☐ *Partially Completed* ☐ *Checked by:* _____

**Part 3**

6. Construct circuit 3 on the breadboard. Connect all inputs (A, B) to a switches and output C, D and F to LEDs.




Circuit 3

Truth Table 7

| A | B | C | D | F |
|---|---|---|---|---|
| 1 | 1 | 0 | 0 | 0 |
| 0 | 1 | 0 | 1 | 1 |
| 1 | 0 | 1 | 0 | 1 |
| 0 | 0 | 0 | 0 | 0 |

7. Test Circuit 3; fill in Truth Table 7 for the circuit outputs (C, D, and F) for all possible input combinations.

8. What single gate does Circuit 3 represent?

XOR GATE