

TUTORIAL 4a

1. Determine the correct number of *Variable*, *Literal* and *Term* for the following Boolean Expression.

$$F = ABC + \overline{AB} + \overline{CD} + \overline{ABC}\overline{E}$$

- A. *Variable* = 4, *Literal* = 4 and *Term* = 11
 B. *Variable* = 5, *Literal* = 7 and *Term* = 4
 C. *Variable* = 4, *Literal* = 11 and *Term* = 5
 D. *Variable* = 5, *Literal* = 5 and *Term* = 11
2. Which of the following Boolean Expressions needs to be applied DeMorgan's Theorem?

- i. $F = ABC + \overline{CD} + \overline{ABC}\overline{E}$ iii. $F = ABC + \overline{AB} + \overline{\overline{CD}} + \overline{ABC}\overline{E}$
 ii. $F = (\overline{A+C}).(\overline{AB} + \overline{AB} + \overline{CE})$ iv. $F = (\overline{\overline{A+C}}).(\overline{AB} + \overline{CE})$

- A. i and ii C. iii and iv
 B. ii and iii D. ii, iii and iv
3. If you are converting a non-standard Products of Sum (POS) to a standard POS form of Boolean Expression, what is the step involved?
- A. Add 1 in the Boolean Expression and use rule $A+1=1$
 B. Add 1 in the Boolean Expression and use rule $A+\overline{A}=1$
 C. Add 0 in the Boolean Expression and use rule $A.0=0$
 D. Add 0 in the Boolean Expression and use rule $A.\overline{A}=0$
4. Determine how to write Products of Sum (POS) term in the following truth table.

A	B	C	Output, F
0	0	1	0
1	0	0	1

- A. Look at output F=0 and write the term as $\overline{A} + \overline{B} + C$
 B. Look at output F=0 and write the term as $A + B + \overline{C}$
 C. Look at output F=1 and write the term as $\overline{A}.\overline{B}.C$
 D. Look at output F=1 and write the term as $A.B.\overline{C}$

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b) You are given a truth table as follows:

Input				Output
A	B	C	D	F
0	0	0	0	1
0	0	0	1	1
0	0	1	0	0
0	0	1	1	0
0	1	0	0	1
0	1	0	1	0
0	1	1	0	1
0	1	1	1	0
1	0	0	0	1
1	0	0	1	1
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	1
1	1	1	1	0

- i. Write the standard Boolean equation for SOP and the Sigma (Σ) representation.
- ii. Write the standard Boolean equation for POS and the Pi (Π) representation.
- iii. Construct the K-Map.
- iv. From the K-Map:
 - a. Get the minimize form for SOP.
 - b. Get the minimize form for POS.
- v. Between the minimize SOP in iv(a) and POS in iv(b), which is more cost effective in the circuit implementation. Comment your answer.