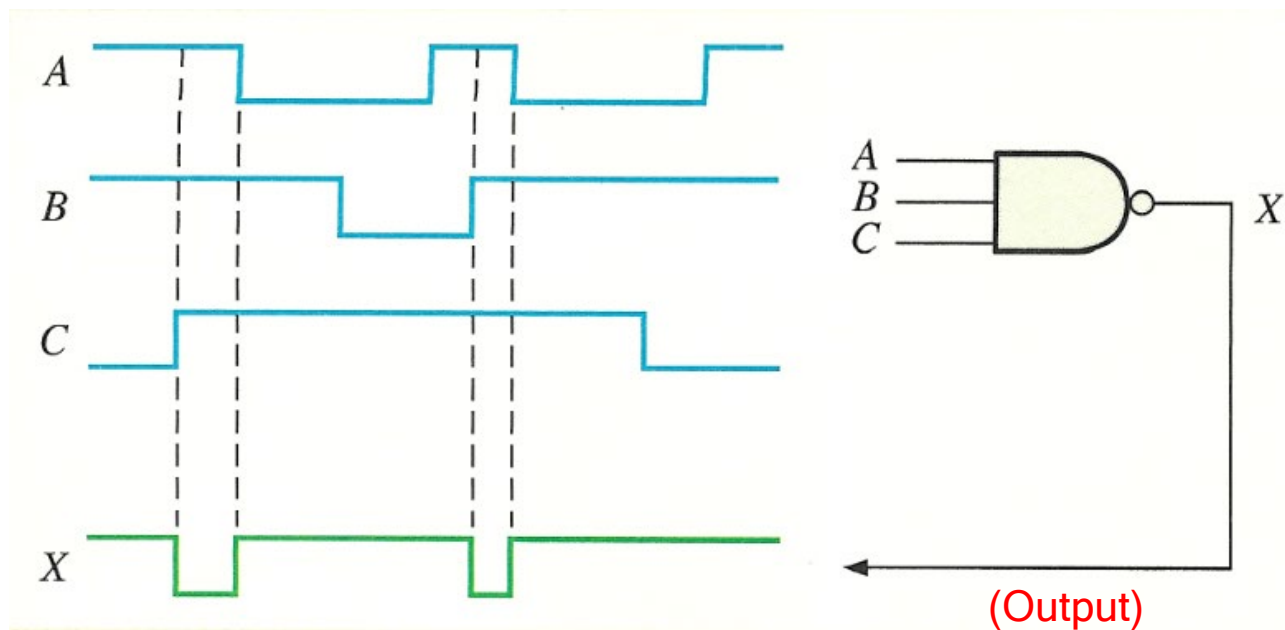


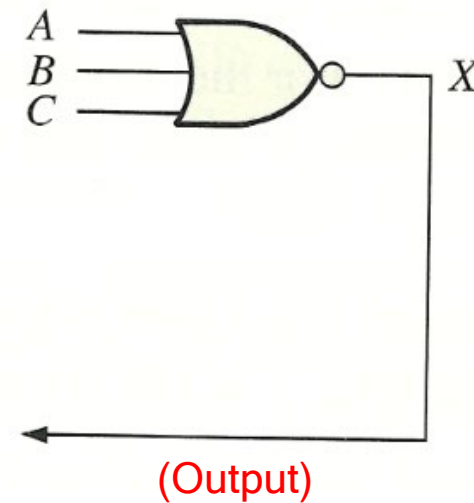
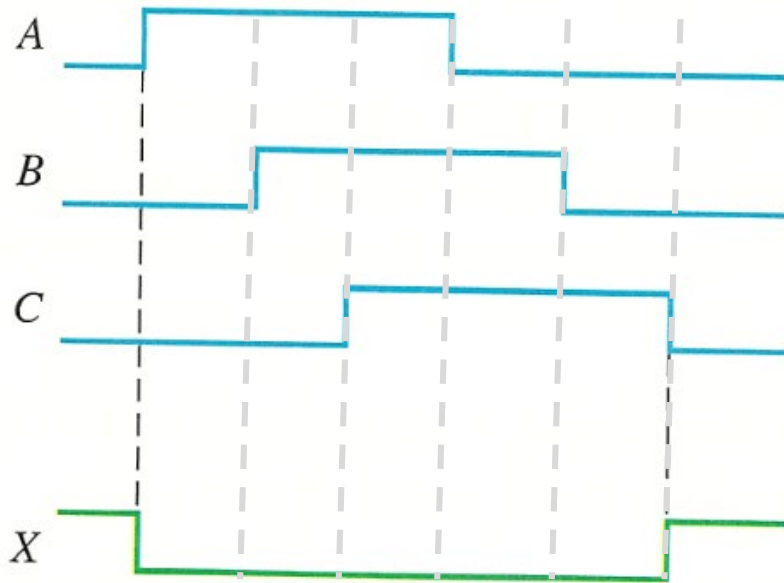
**Exercise 3.3:**

Show the output waveform for the 3-input NAND gate below with its proper time relationship to the inputs.



**Exercise 3.4:**

Show the output waveform for the 3-input NOR gate below with its proper time relationship to the inputs.



### Exercise 3.5:

Determine the output waveforms for the XOR gate and XNOR gate, given the input waveforms, A and B.

