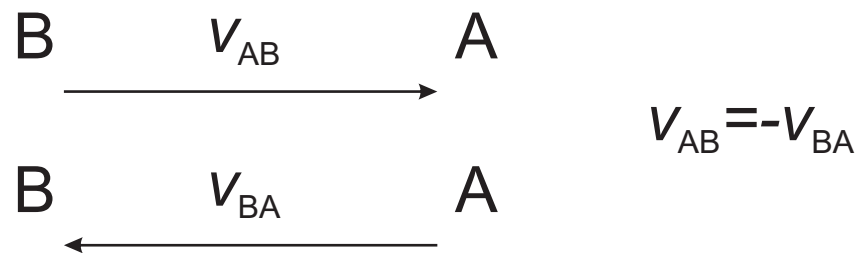


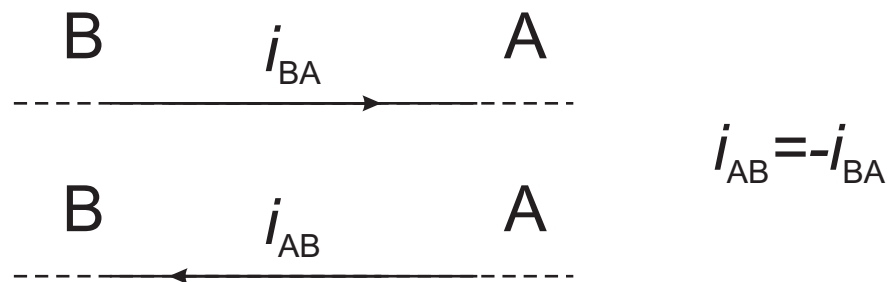
Notes on Basic Circuit Theory

Voltage



It is the difference in electrical potential between two points

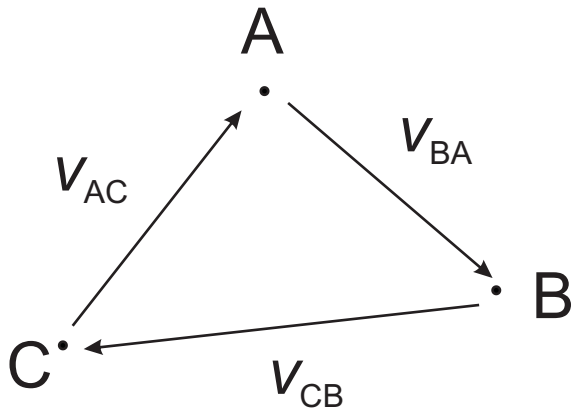
Current



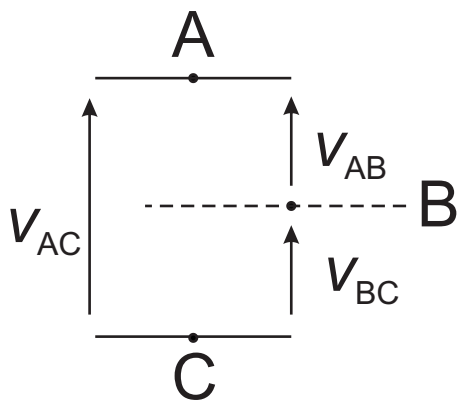
It is the charge flowing in a section of a conductor per unit time:

$$i = \frac{dQ}{dt}$$

Kirchoff's Voltage Law (KLV)

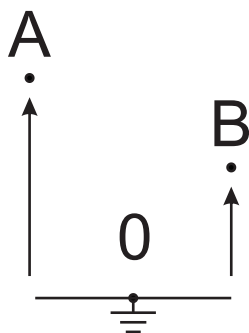


$$V_{BA} + V_{CB} + V_{AC} = 0$$



$$V_{AC} = V_{BC} + V_{AB}$$

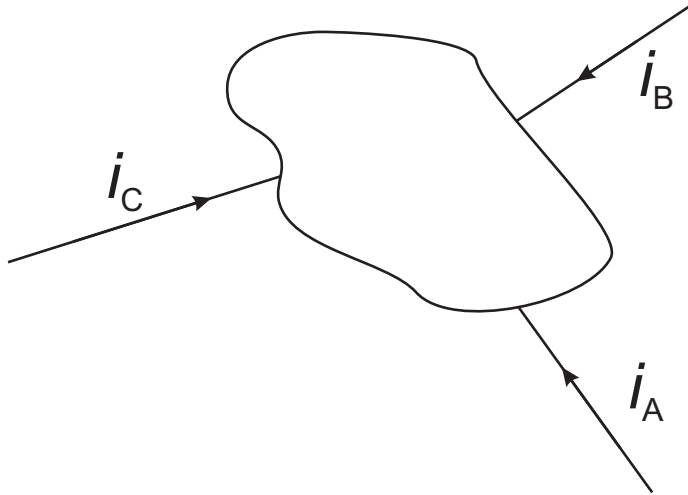
like "quotes" on a drawing



$$V_A = V_{A0}$$

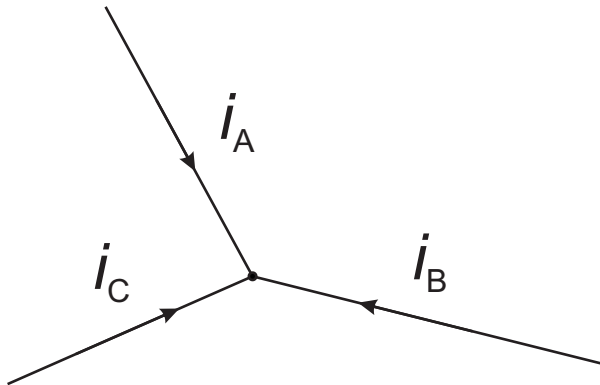
$$V_B = V_{B0}$$

Kirchoff's Current Law (KLI)

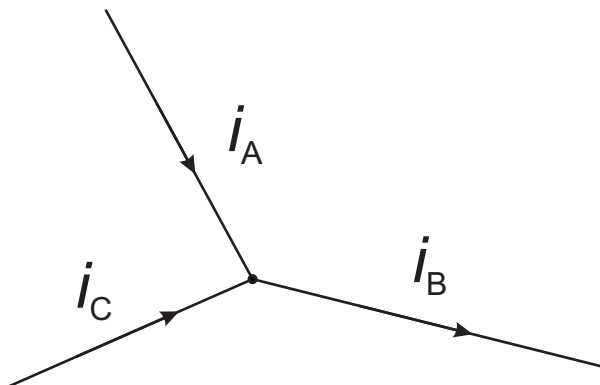


$$i_A + i_B + i_C = 0$$

special case: Node's Law



$$i_A + i_B + i_C = 0$$

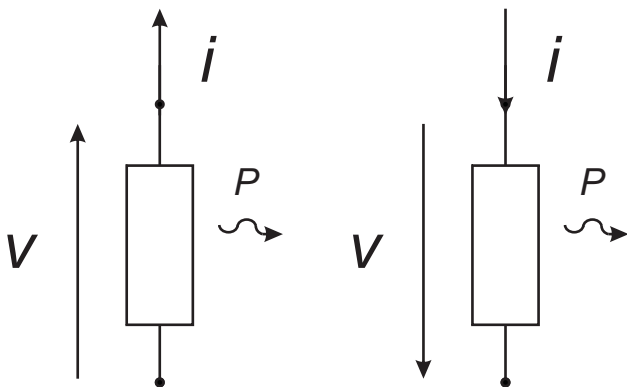


$$i_B = i_A + i_C$$

like a flow of a liquid

Sign Conventions

Active Sign Convention:

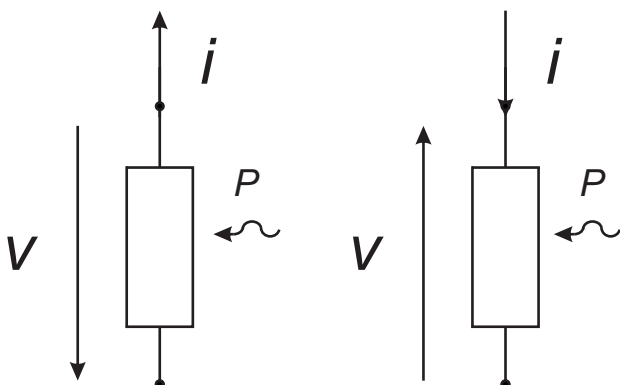


v and i arrows have *the same direction*

$$P = v i$$

P : electrical power *delivered by* the device

Passive Sign Convention:



v and i arrows have *opposite directions*

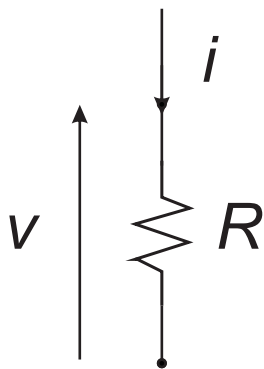
$$P = v i$$

P : electrical power *absorbed by* the device

Basic Circuit Elements

Ideal Resistor

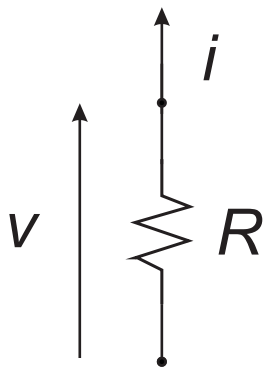
Passive Sign Convention:



$$v = Ri$$

Ohm's Law

Active Sign Convention:



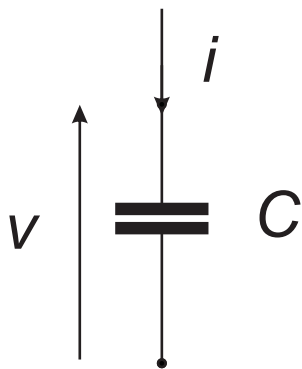
$$v = -Ri$$

Ohm's Law

Basic Circuit Elements

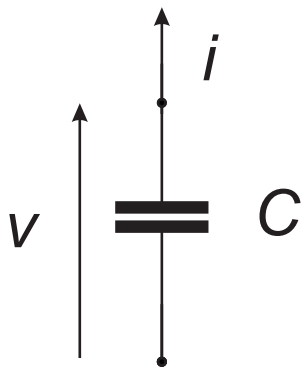
Ideal Capacitor

Passive Sign Convention:



$$i = C \frac{dv}{dt}$$

Active Sign Convention:

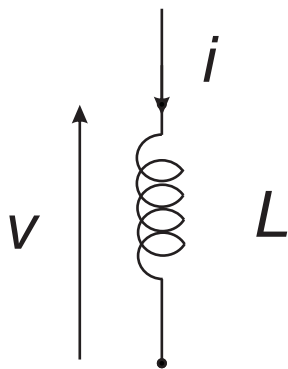


$$i = -C \frac{dv}{dt}$$

Basic Circuit Elements

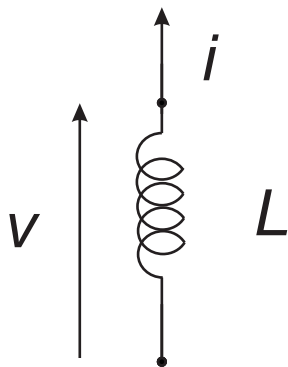
Ideal Inductor

Passive Sign Convention:



$$v = L \frac{di}{dt}$$

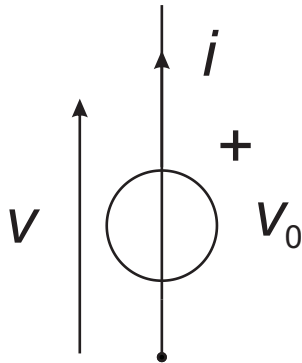
Active Sign Convention:



$$v = -L \frac{di}{dt}$$

Basic Circuit Elements

Ideal Voltage Source



$$V = V_0$$

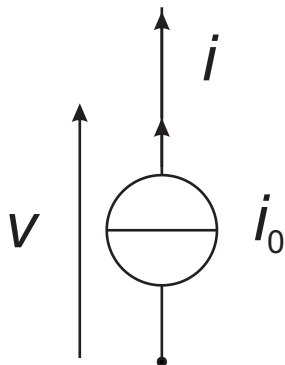
independently of i

special case: if $V_0=0$



short circuit (i.e. an ideal wire)

Ideal Current Source



$$i = i_0$$

independently of v

special case: if $i_0=0$



open circuit (i.e. no electrical connection)

Basic Circuit Elements

Switch

ON state



equivalent to a short circuit
(i.e. an ideal wire)

OFF state



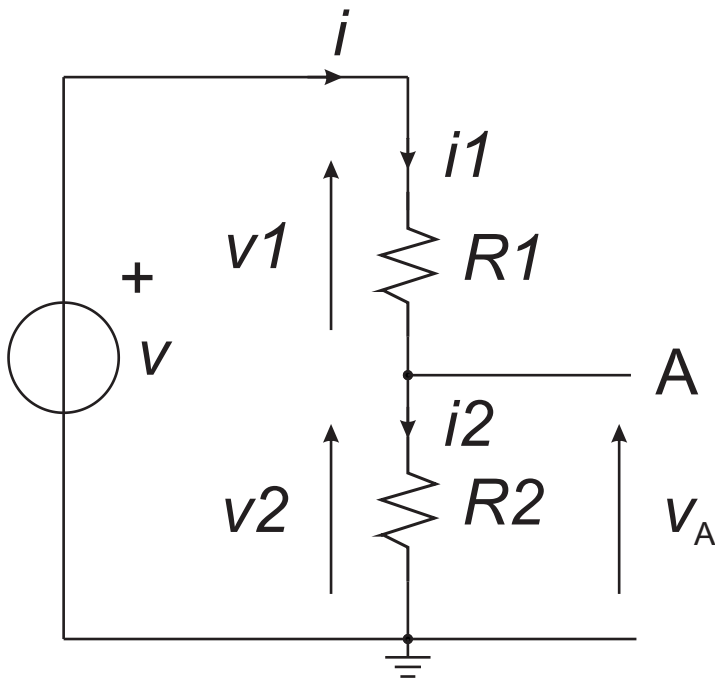
equivalent to an open circuit
(i.e. two unconnected points)

Resistor Connections

Series Connection

Two resistors are connected in series if

- they share one terminal (A),
- such a terminal is not connected to nothing else



by Ohm's Law

$$v_1 = R_1 i_1$$

$$v_2 = R_2 i_2$$

by KLI

$$i = i_1 = i_2$$

by KLV

$$v = v_1 + v_2$$

as a consequence

$$\begin{aligned} v &= R_1 i + R_2 i \\ &= (R_1 + R_2) i \quad (*) \\ &= R_{EQ} i \end{aligned}$$

Two resistors in series are equivalent (for external effects)
to a single resistor with a resistance

$$R_{EQ} = R_1 + R_2$$

Replacing two resistor in series with the equivalent resistor, node A is lost.

Node A voltage can be expressed in terms of the overall voltage v across the series-connected resistors

$$v_A = v_2 = R_2 i_2 = R_2 i, \quad \text{from } (*): \quad i = \frac{v}{R_1 + R_2}$$

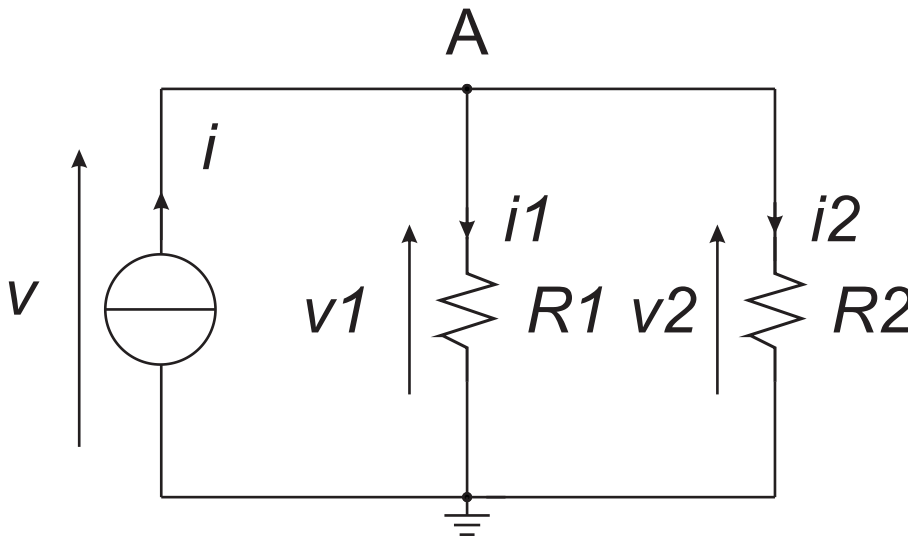
$$v_A = \frac{R_2}{R_1 + R_2} v$$

Voltage Divider Rule

Resistor Connections

Shunt Connection

Two resistors are connected in shunt if they share both terminals



by Ohm's Law

$$v1=R1 \ i1 \quad v2=R2 \ i2$$

by KLI

$$i=i1+i2$$

by KLV

$$v=v1=v2$$

as a consequence

$$\begin{aligned} i &= v/R1 + v/R2 \\ &= v(1/R1+1/R2) \\ &= v/R_{EQ} \end{aligned} \quad (*)$$

Two shunt-connected resistors are equivalent (for external effects) to a single resistor with a resistance

$$R_{EQ} = \frac{1}{\frac{1}{R1} + \frac{1}{R2}} = \frac{R1R2}{R1+R2}$$

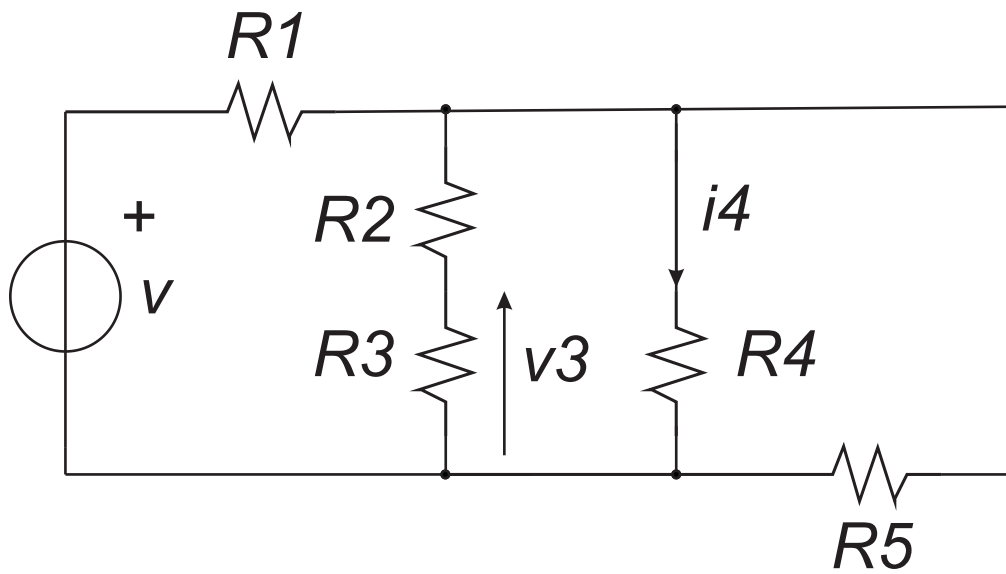
Current $i1$ can be expressed in terms of the overall current i

$$i1 = v1/R1 = v/R1 \quad \text{from } (*): \quad v = \frac{R1R2}{R1+R2} i$$

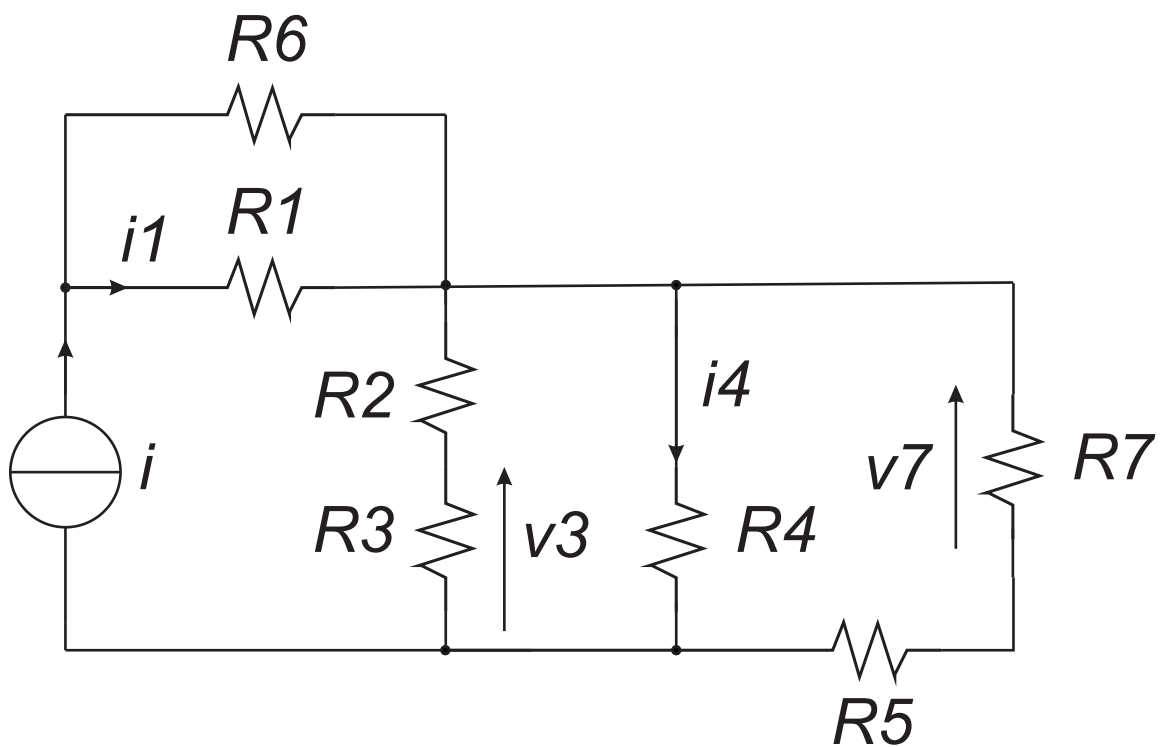
$$i1 = \frac{R2}{R1+R2} i$$

Current Divider Rule

Exercises

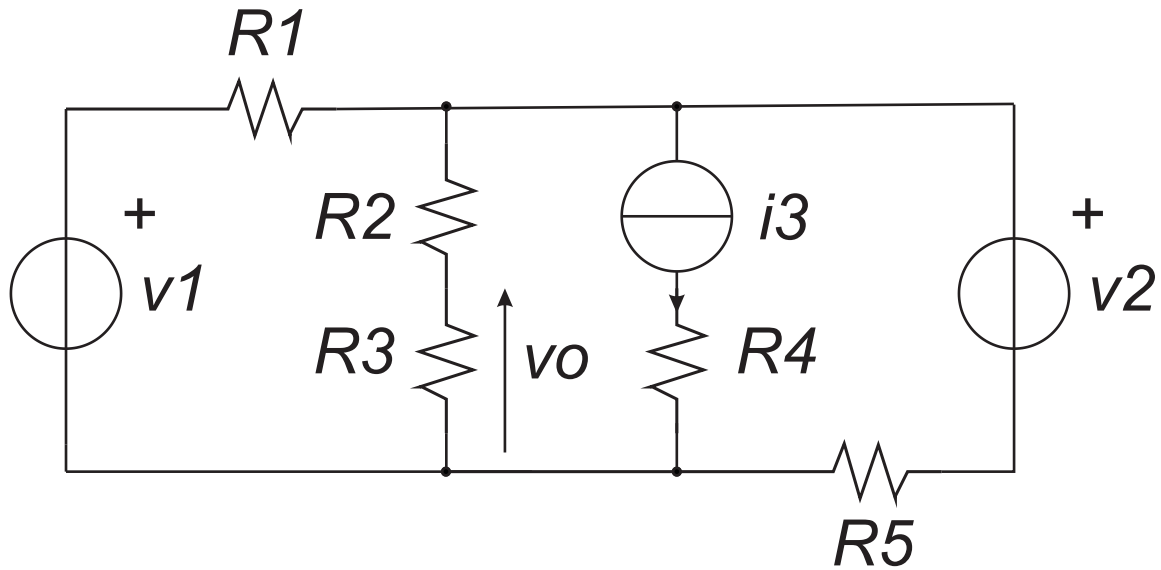


Ex. 1: Evaluate $v3$ and $i4$



Ex.2: Evaluate $i1$, $v3$, $i4$ and $v7$

Superposition Principle



In any **linear** circuit which includes more than one independent source, each output can be expressed as the sum of the contributions of each source.

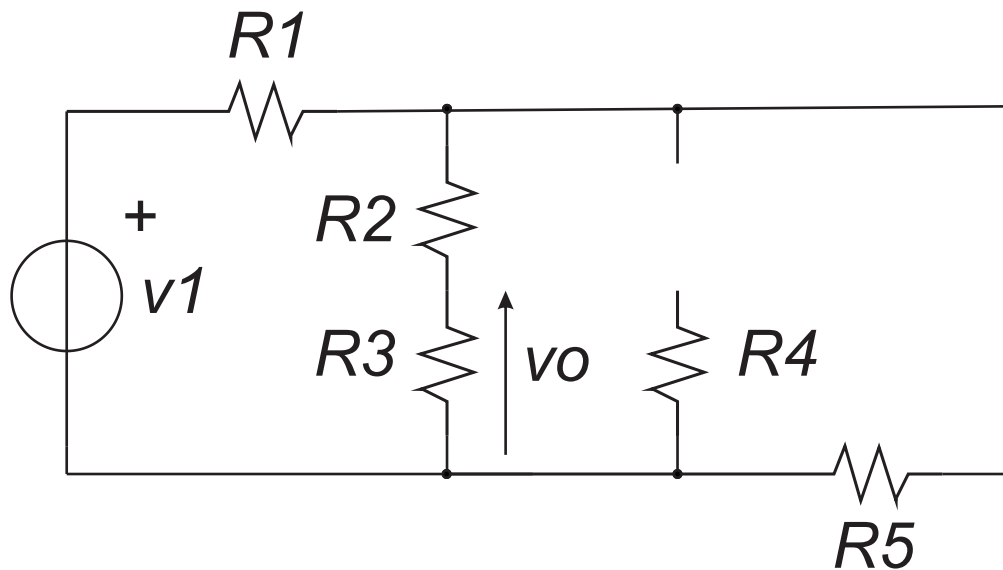
$$V_O = V_O' + V_O'' + V_O'''$$

The contribution of a source to the output is the value of the output in the circuit when the considered source is active, while the other sources are turned off.

a **voltage source**, when it is turned off, is equivalent to a **short circuit**
a **current source**, when it is turned off, is equivalent to an **open circuit**

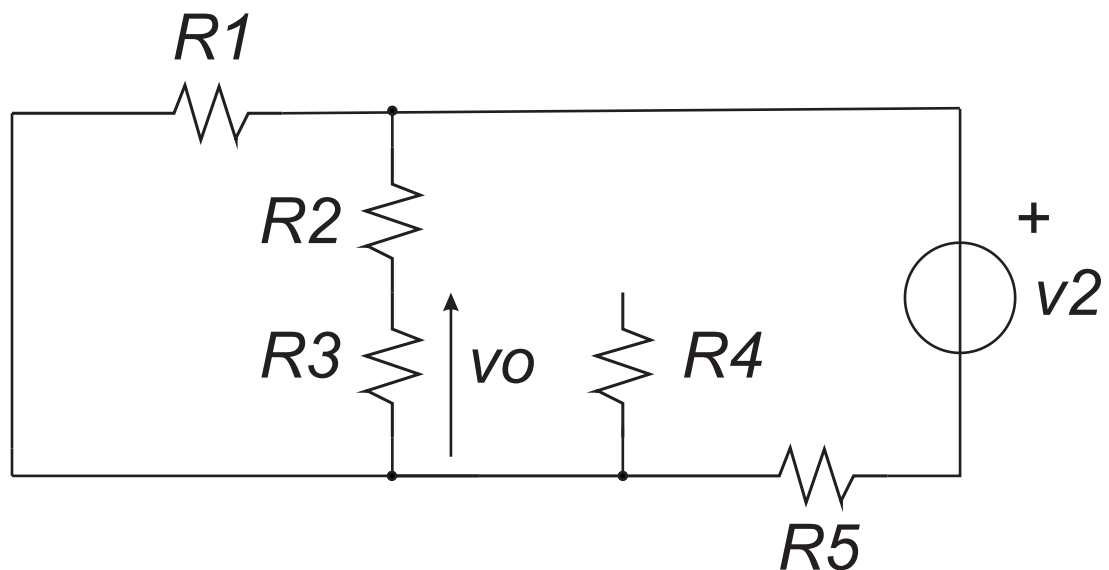
Superposition Principle

Contribution of v_1 ($v_{o'}$)



$$v_{o'} = \frac{(R_2 + R_3) // R_5}{R_1 + (R_2 + R_3) // R_5} \cdot \frac{R_3}{R_2 + R_3} v_1$$

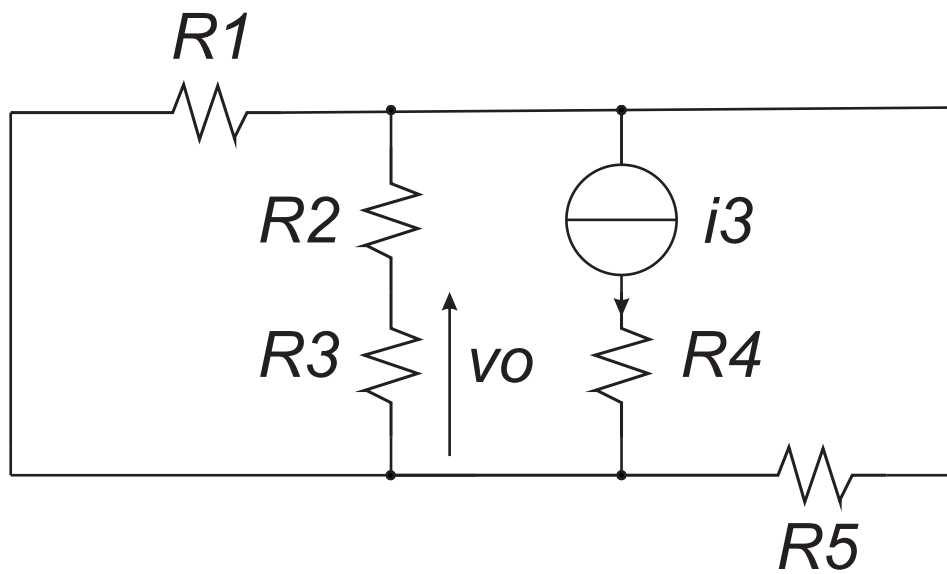
Contribution of v_2 ($v_{o''}$)



$$v_{o''} = \frac{(R_2 + R_3) // R_1}{R_5 + (R_2 + R_3) // R_1} \cdot \frac{R_3}{R_2 + R_3} v_2$$

Superposition Principle

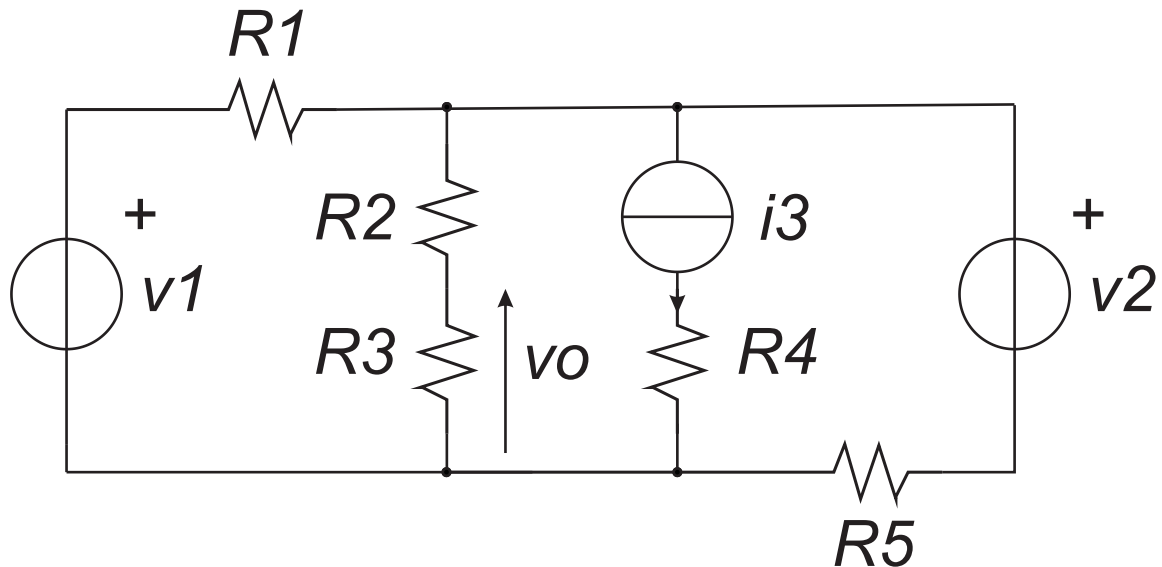
Contribution of i_3 (v_o''')



$$v_o''' = - R_1 // R_5 // (R_2 + R_3) \frac{R_3}{R_2 + R_3} i_3$$

Superposition Principle

Overall Circuit



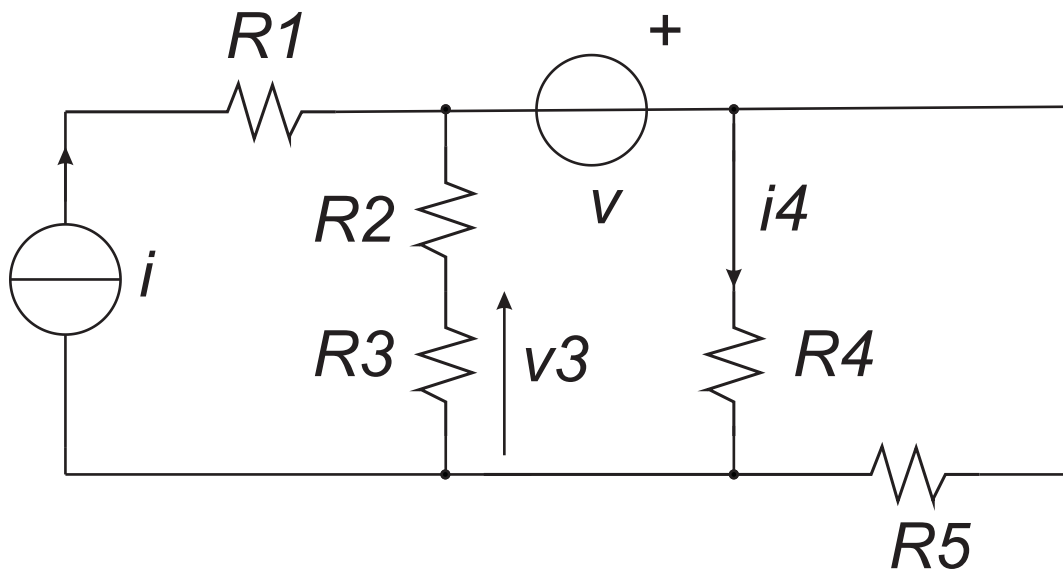
$$v_0 = v_0' + v_0'' + v_0'''$$

$$v_0 = \frac{(R_2 + R_3) // R_5}{R_1 + (R_2 + R_3) // R_5} \frac{R_3}{R_2 + R_3} v_1 +$$

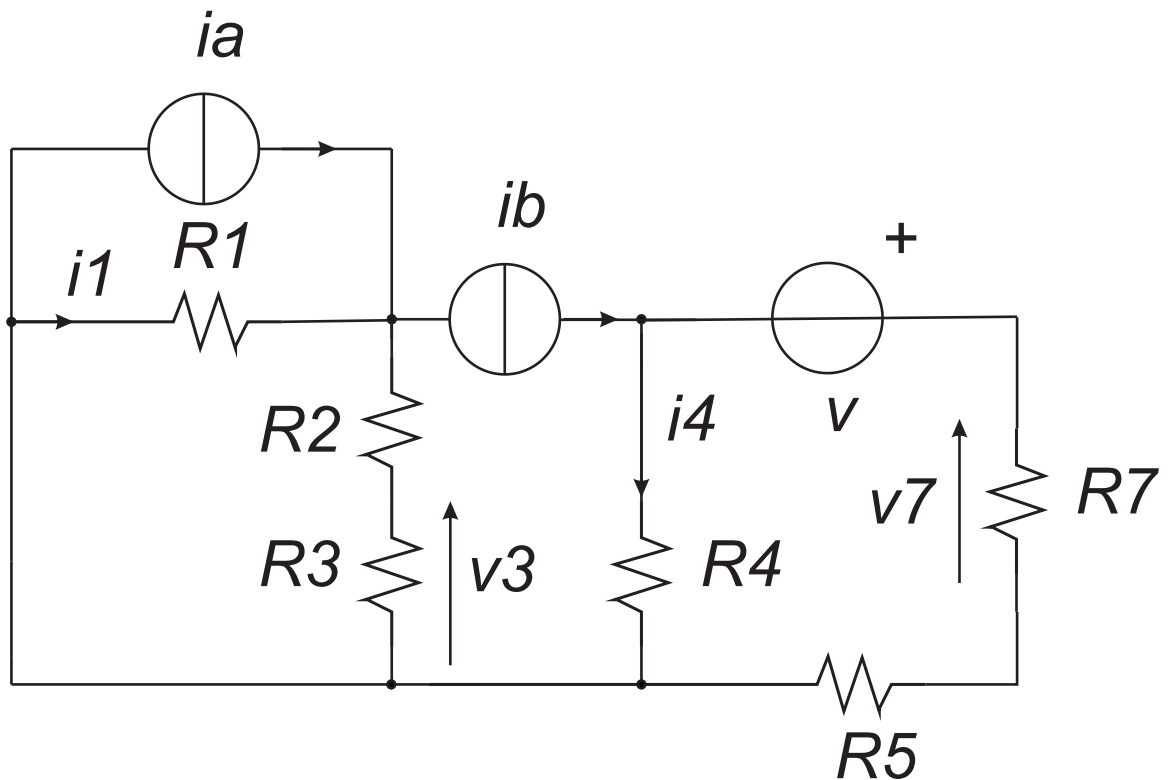
$$+ \frac{(R_2 + R_3) // R_1}{R_5 + (R_2 + R_3) // R_1} \frac{R_3}{R_2 + R_3} v_2 +$$

$$- R_1 // R_5 // (R_2 + R_3) \frac{R_3}{R_2 + R_3} i_3$$

Exercises



Ex. 1: Evaluate $v3$ and $i4$



Ex.2: Evaluate $i1$, $v3$, $i4$ and $v7$